	Application No.	Applicant(s)
	10/029,146	CHOO ET AL.
Notice of Allowability	Examiner	Art Unit
	Mike Qi	2871
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. X This communication is responsive to 7/15/04.		
2. The allowed claim(s) is/are 1-18.		
3. The drawings filed on <u>28 December 2001</u> are accepted by the Examiner.		
 4.		
Attachment(s) 1. ☑ Notice of References Cited (PTO-892) 2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948) 3. ☐ Information Disclosure Statements (PTO-1449 or PTO/SB/O Paper No./Mail Date 4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material	6. ☐ Interview Summary Paper No./Mail Dat 08), 7. ☐ Examiner's Amendr	

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DETAILED ACTION

Allowable Subject Matter

- 1. Claims 1-18 are allowed.
- 2. The following is an examiner's statement of reasons for allowance:

The prior art of record neither discloses nor teaches that a LCD device driving input line structure comprising various elements as claimed, more specifically, as the following:

in the driving line input region, a pixel electrode on the passivation layer to electrically connect the first, second and third line layers through each contact hole, and the first, second and third layers providing signal to driver ICs, and each driver ICs applying output signals to gate lines or data lines [claims 1 and 13; as shown in Figs.4-5];

The closest references such as US 5,523,866 (Morimoto et al) and US 2002/0101398 (Fujita) discloses that the pixel electrode connected to drain electrode and a contact serves to connect the gate line to the gate electrode, and a contact hole serves to connect the drain terminal to drain electrode, and all of the contact hole functions as the connection in the cell array region, but the references do not disclose the contact holes functions as the connection in the drive input region as claimed, such as the first, second and third line layers through contact holes to connect pixel electrodes to providing signals from the driver ICs.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably Application/Control Number: 10/029,146

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accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

3. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Mike Qi whose telephone number is (571) 272-2299. The examiner can normally be reached on M-T 8:00 am-5:00 pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Robert Kim can be reached on (571) 272-2293. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Mike Qi Aug. 3, 2004

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PRIMABY EXAMINER

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